

# Computer aided verification

## Lecture 4: Model checking for LTL

(i)  $M \mapsto \mathcal{A}_M$

(ii)  $\neg\phi \mapsto \mathcal{A}_{\neg\phi}$

( not  $\phi \mapsto \mathcal{A}_\phi \mapsto \bar{\mathcal{A}}_\phi$  )

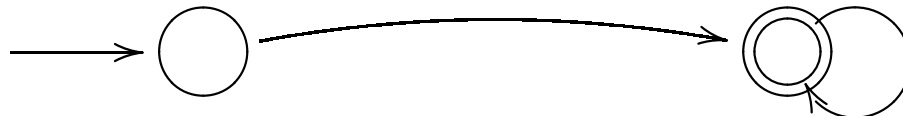
(iii)  $L_\omega(\mathcal{A}_M) \cap L_\omega(\mathcal{A}_{\neg\phi}) = \emptyset ?$

( not  $L_\omega(\mathcal{A}_M) \subseteq L_\omega(\mathcal{A}_\phi)$  )

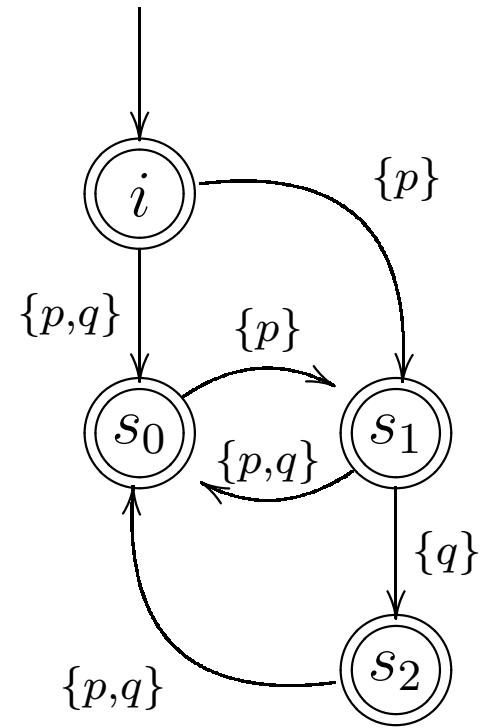
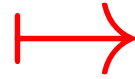
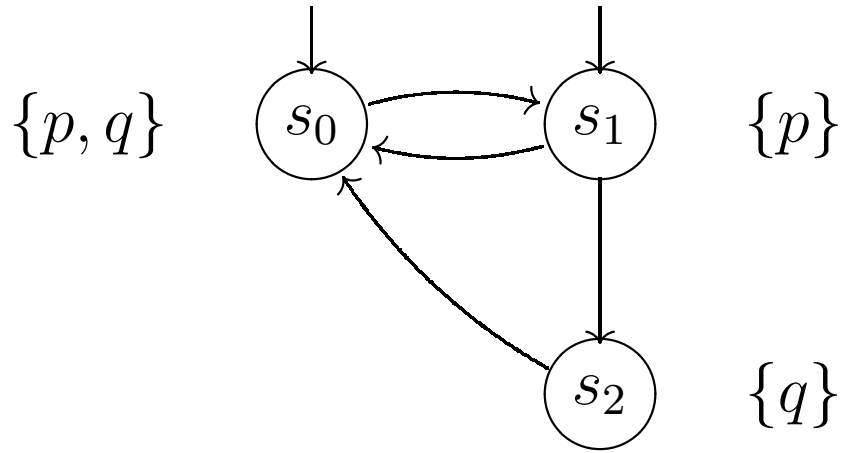
$L_\omega(\mathcal{A}_M \times \mathcal{A}_{\neg\phi}) = \emptyset ?$

yes  $\rightarrow M \models \phi$

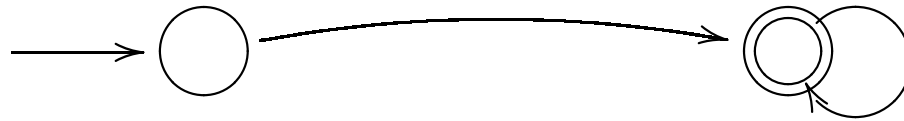
no  $\rightarrow \neg(M \models \phi)$ , counterexample = a path in  $M$

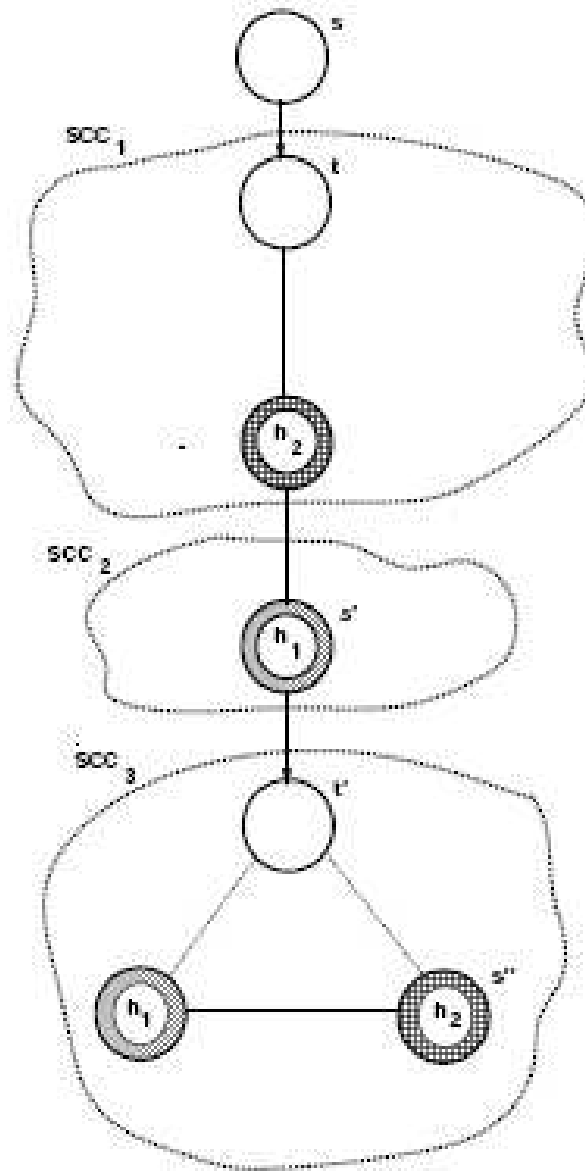


$$(i) \quad M \mapsto \mathcal{A}_M$$



(iii)  $L_\omega(\mathcal{A}) \neq \emptyset?$

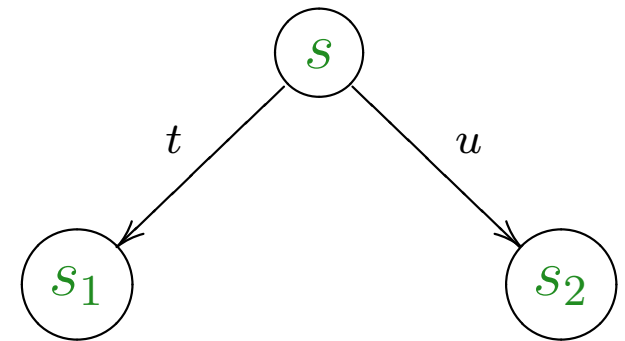




[Clarke, Grumberg, Peled 2000]

(1) On the fly verification

for **each** successor  $s_i$  of  $s$  do ...



...

Safety: DFS or BFS

```
proc dfs(s)
  if error(s) then report error fi
  add s to Statespace
  for each successor t of s do
    if t not in Statespace then dfs(t) fi
  od
end
```

[Holzmann, Peled, Yannakakis 1996]



```
proc dfs(s)
  if error(s) then report error fi
  add {s,0} to Statespace
  for each successor t of s do
    if {t,0} not in Statespace then dfs(t) fi
  od
  if accepting(s) then seed:=s; ndfs(s) fi
end
proc ndfs(s) /* the nested search */
  add {s,1} to Statespace
  for each successor t of s do
    if {t,1} not in Statespace then ndfs(t) fi
    else if t==seed then report cycle fi
  od
end
```

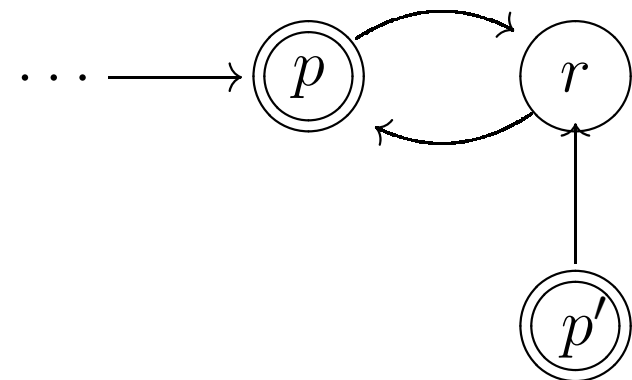
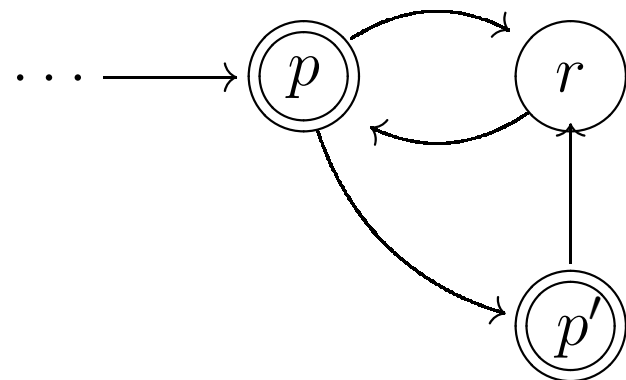
[Holzmann,Peled,Yannakakis 1996]

# Proof of correctness

Assume an accepting state  $p$  with a cycle not detected by  $\text{ndfs}(p)$ . Let  $r$  – the first such state.

Let  $r$  – the first state inspected by  $\text{ndfs}(p)$  that is on a  $p$ -cycle and for which  $\{r, 1\}$  in Statespace.

Let  $p'$  – the accepting state such that  $r$  visited by  $\text{ndfs}(p')$ .

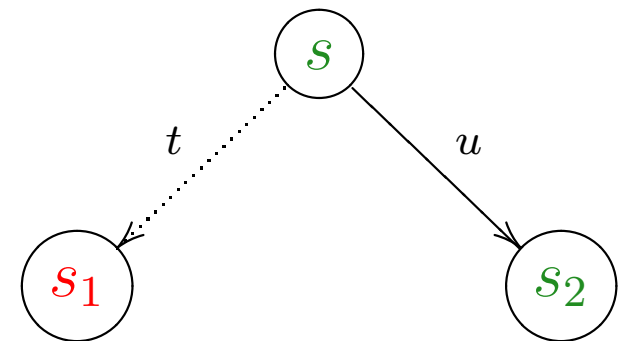


(1) On the fly verification

for **each** successor  $s_i$  of  $s$  do ...

(2) Partial-order reductions

for **each selected** successor  $s_i$  of  $s$  do ...



**selected** – depends on states visited so far !

```
proc dfs(s)
  if error(s) then report error fi
  add {s,0} to Statespace
  add s to Stack
  for each (selected) successor t of s do
    if {t,0} not in Statespace then dfs(t) fi
  od
  if accepting(s) then ndfs(s) fi
  delete s from Stack
end
proc ndfs(s) /* the nested search */
  add {s,1} to Statespace
  for each (selected) successor t of s do
    if {t,1} not in Statespace then ndfs(t) fi
    else if t in Stack then report cycle fi
  od
end
```

[Holzmann,Peled,Yannakakis 1996]

# np-cycles: FG $\rightarrow$ progress

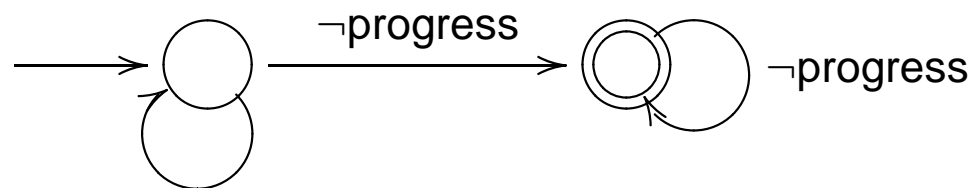
```
proc dfs(s)
  if error(s) then report error fi
  add {s,0} to Statespace
  for each successor t of s do
    if {t,0} not in Statespace then dfs(t) fi
  od
  ndfs(s) /* different */
end
proc ndfs(s) /* the nested search */
  if s is Progress State then return fi /* new */
  add {s,1} to Statespace
  add s to Stack /* new */
  for each successor t of s do
    if {t,1} not in Statespace then ndfs(t) fi
    else if t is in Stack then report cycle fi /* different */
  od
  delete s from Stack /* new */
end
```

[Holzmann,Peled,Yannakakis 1996]

# np-cycles: automaton

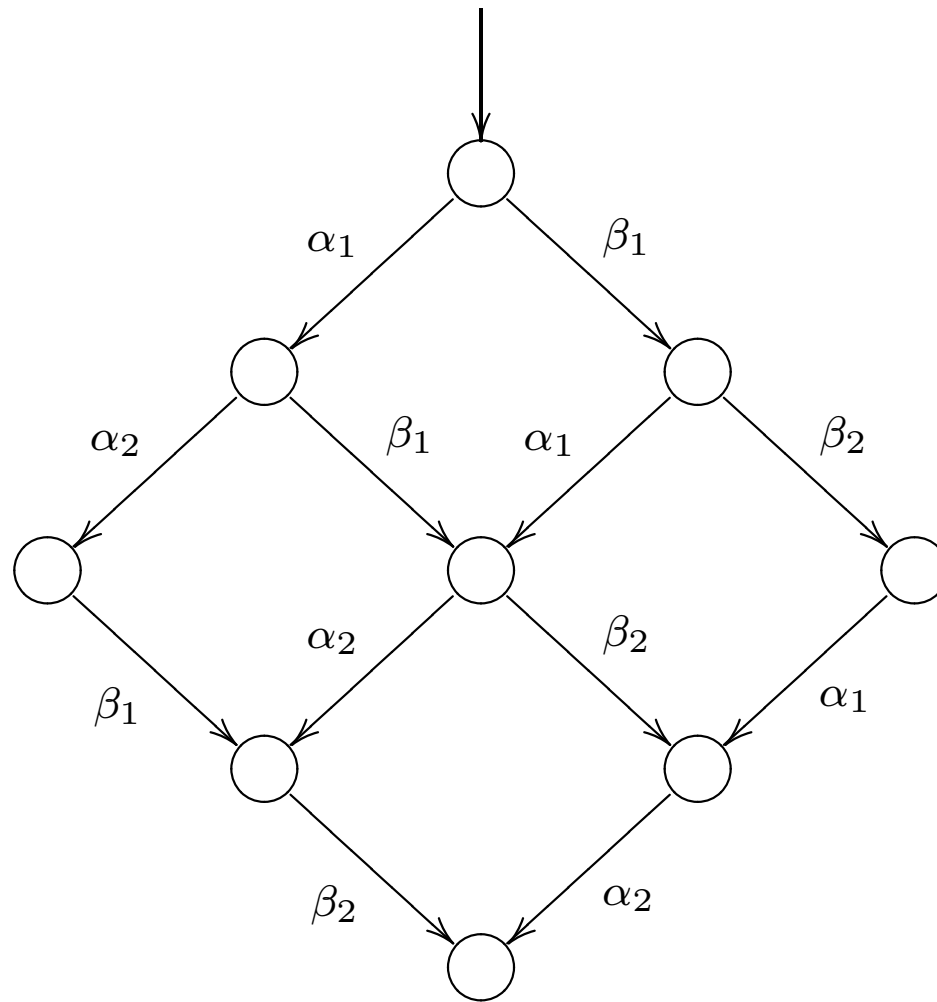
```
never { /* non-progress:  $\diamond \square \neg progress$  */  
  do  
    :: skip  
    :: !progress - > break  
  od;  
accept: do  
  :: !progress  
od  
}
```

[Holzmann, Peled, Yannakakis 1996]



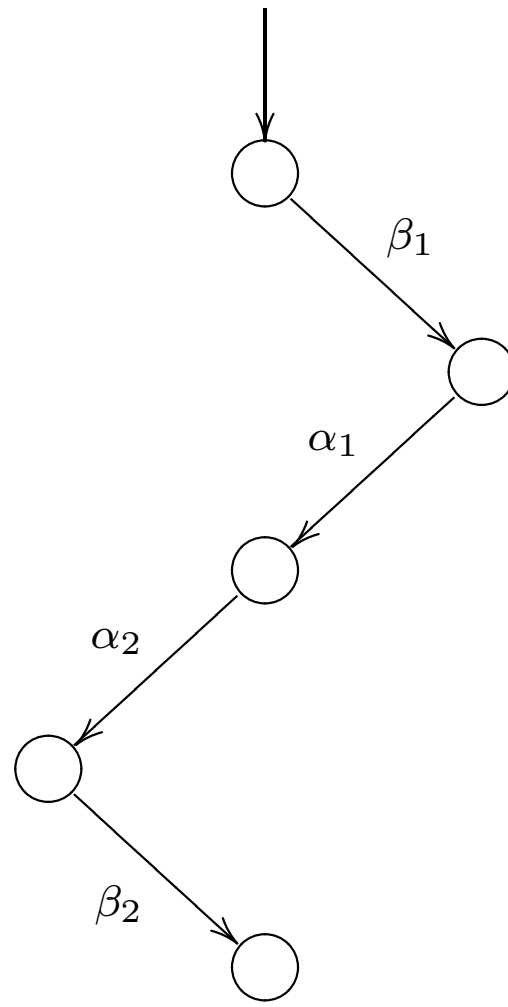
(co-Büchi  $\subseteq$  Büchi)

# Partial-order reductions

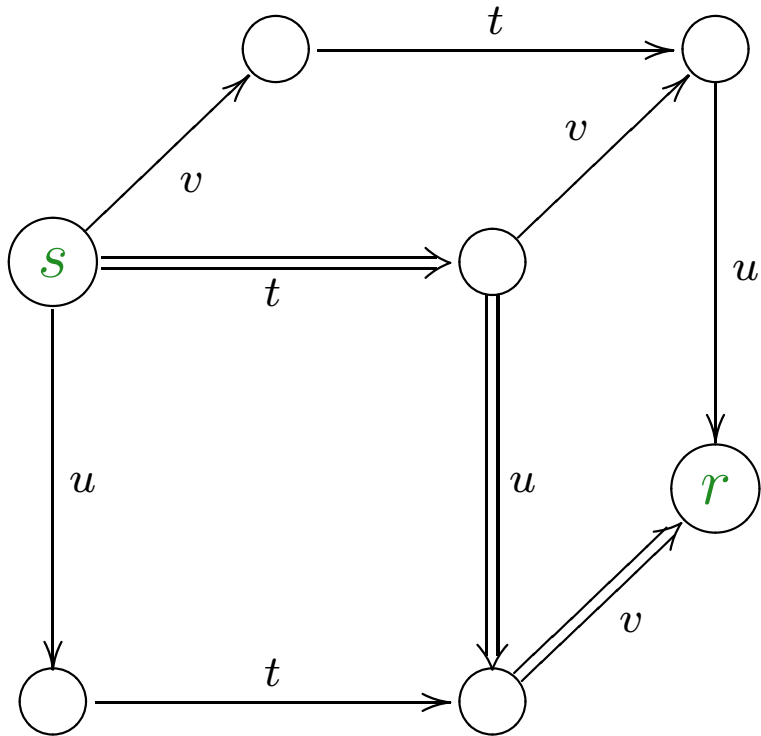




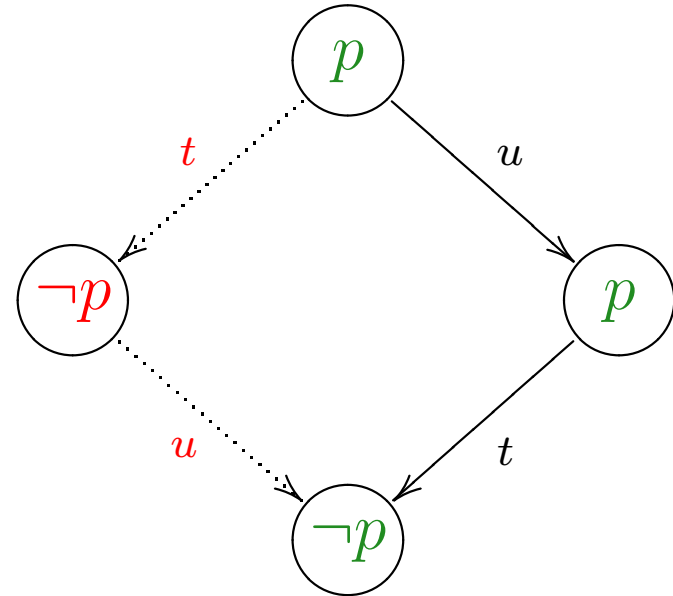
# Motivation



# Motivation



$F \neg p$



$t, u$  niezależne

**Def.:**  $M = \langle S, S_{\text{init}}, T, L \rangle$   $T$  – operations (transitions)

for  $\alpha \in T$ :  $\text{en}_\alpha \subseteq S$ ,  $\alpha : \text{en}_\alpha \rightarrow S$  (determinism)

**path:**  $\Pi = s_0 \xrightarrow{\alpha_0} s_1 \xrightarrow{\alpha_1} s_2 \xrightarrow{\alpha_2} \dots$   $s_0 = S_{\text{init}}$

$\alpha_i(s_i) = s_{i+1}$

$\text{en}_s := \{ \alpha \mid s \in \text{en}_\alpha \}$   $(\alpha \in \text{en}_s \iff s \in \text{en}_\alpha)$

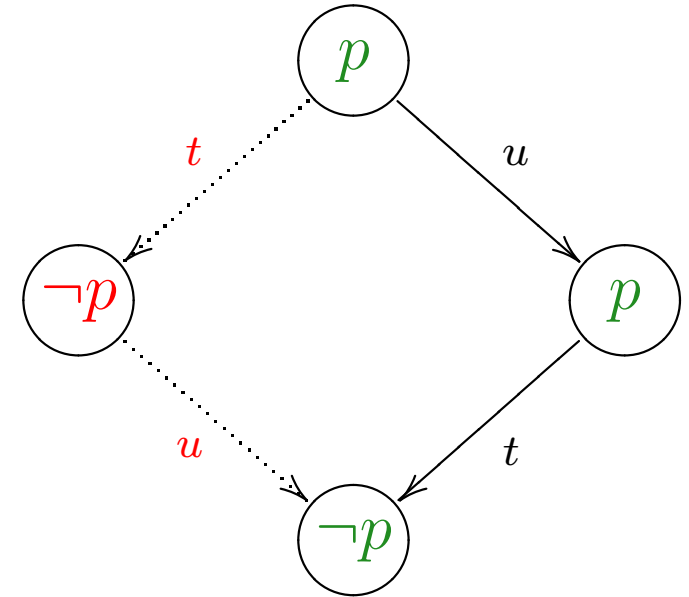
**Idea:**  $\text{ample}_s \subseteq \text{en}_s$  instead of  $\text{en}_s$  in double DFS ?

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This makes sense, when:

- the result of verification is the same (correctness)
- significantly less states visited
- time overhead reasonable (effectivity)

When may we ignore  $t$  ?



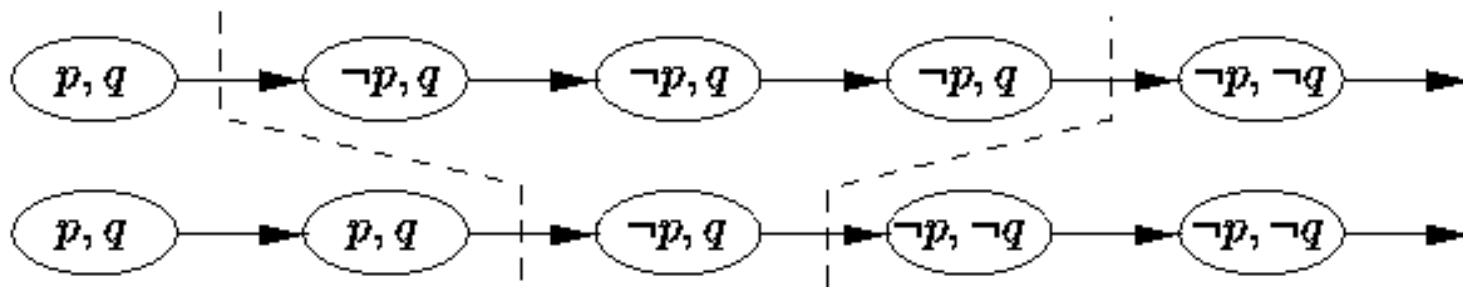
**Problem 1:** Property may depend on state  $\textcircled{\neg p}$  .

**Problem 2:**  $\textcircled{\neg p}$  -successors unreachable otherwise.

**Def.:**  $\Pi = s_0 \rightarrow s_1 \rightarrow s_2 \rightarrow \dots$  i  $\Pi' = s'_0 \rightarrow s'_1 \rightarrow s'_2 \rightarrow \dots$  are stuttering equivalent,  $\Pi \equiv \Pi'$ , if sequences

$$L(s_0), L(s_1), L(s_2), \dots \quad L(s'_0), L(s'_1), L(s'_2), \dots$$

become identical after grouping is done:



**Def.:**  $M \equiv M'$  if and only if

- $\forall \Pi w M \quad \exists \Pi' w M' \quad \Pi \equiv \Pi'$
- $\forall \Pi' w M' \quad \exists \Pi w M \quad \Pi \equiv \Pi'$

LTL<sub>-X</sub> = LTL without X

**Thm:** If  $\phi \in \text{LTL}_{-X}$  and  $\Pi \equiv \Pi'$ , then  $\Pi \models \phi \iff \Pi' \models \phi$

**Thm:** If  $\phi \in \text{LTL}_{-X}$  and  $M \equiv M'$ , then  $M \models \phi \iff M' \models \phi$

**Thm:**  $\text{LTL}_{-X} = \text{FO}_{\equiv}$



$$M \equiv M'$$



# Sufficient condition for correctness

**(C0)**  $\text{ample}_s = \emptyset \iff \text{en}_s = \emptyset$

**(C1)** ...

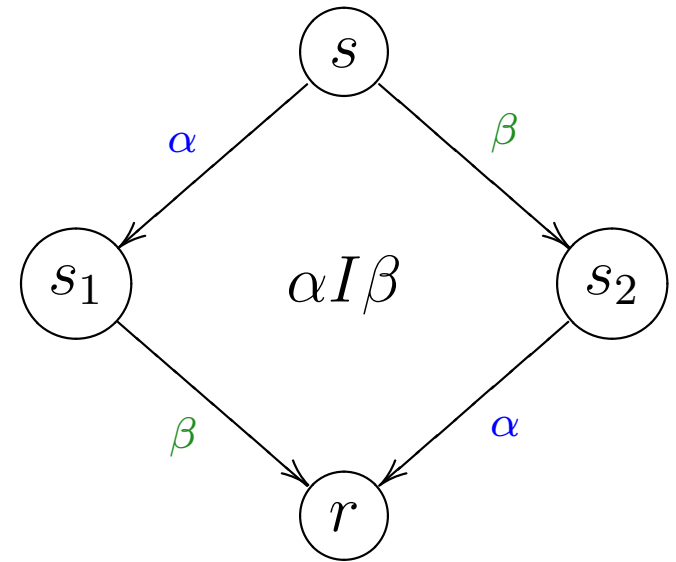
**(C2)** ...

**(C3)** ...

**Def.:**  $\alpha$  is **invisible** if  $L(s) = L(\alpha(s)), \forall s \in \text{en}_\alpha$ .

**Przykład:** If  $\alpha$  invisible, then

$$s s_1 r \equiv s s_2 r$$



# Sufficient condition for correctness

(C0)  $\text{ample}_s = \emptyset \iff \text{en}_s = \emptyset$

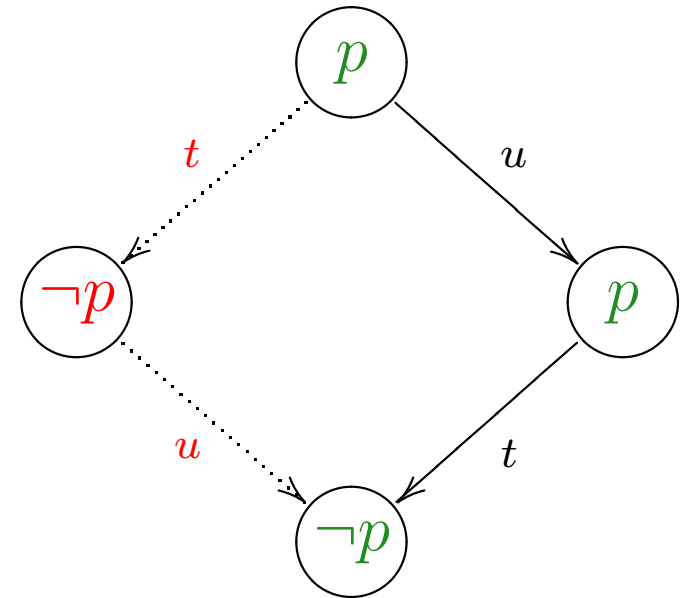
(C1) if  $\text{ample}_s \neq \text{en}_s$  then every  $\alpha \in \text{ample}_s$  is invisible

(C2) ...

(C3) ...

**Idea:** Instead of doing sth now, do it in future!

**Problem 1:** Property may depend on state  $\neg p$ .



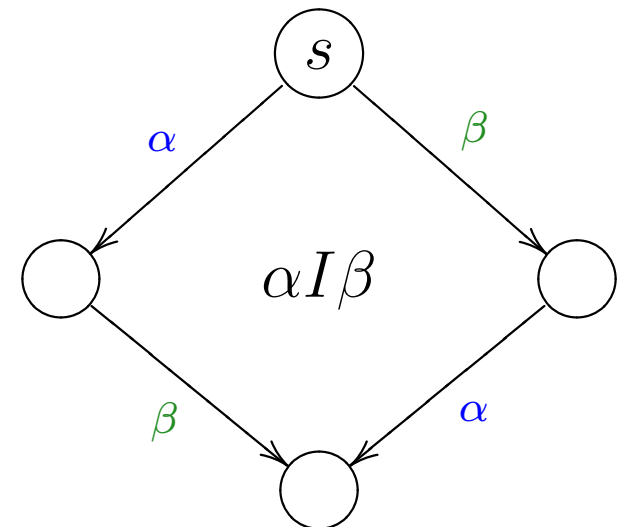
Solved due to **(C1)** !

**(C1)** if  $\text{ample}_s \neq \text{en}_s$ , then every  $\alpha \in \text{ample}_s$  is invisible

**Def.:** Relation of independence  $I \subseteq T \times T$ :

- irreflexive and antisymmetric
- if  $\alpha I \beta$ ,  $\alpha \in \text{en}_s$ ,  $\beta \in \text{en}_s$ , then
  - $\beta(s) \in \text{en}_\alpha$ ,  $\alpha(s) \in \text{en}_\beta$
  - $\beta(\alpha(s)) = \alpha(\beta(s))$

$$(s \in \text{en}_\alpha \cap \text{en}_\beta)$$



$$D = T \times T \setminus I \quad (\text{dependency})$$

**Example:** Independent **may be:**

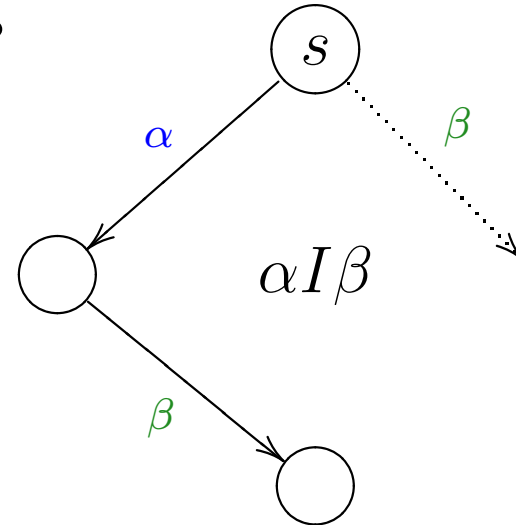
- 2 instructions of different processes operating on local variables
- 2 instructions of different processes that increment the same global variable
- 2 instructions of different processes writing to/reading from different buffers

**Example:** Independent **may be:**

- 2 instructions of different processes operating on local variables
- 2 instructions of different processes that increment the same global variable
- 2 instructions of different processes writing to/reading from different buffers
- 2 instructions of **the same process** ?

**Question:** Let  $\alpha I \beta$ . Is it possible that

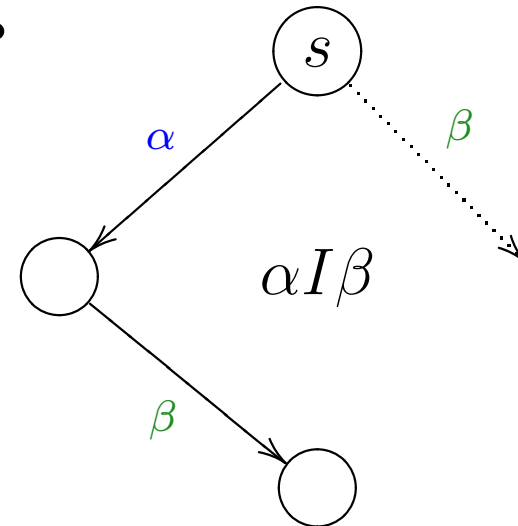
$$s \in \text{en}_\alpha \setminus \text{en}_\beta \quad \alpha(s) \in \text{en}_\beta ?$$





**Question:** Let  $\alpha I \beta$ . Is it possible that

$$s \in \text{en}_\alpha \setminus \text{en}_\beta \quad \alpha(s) \in \text{en}_\beta ?$$



**Yes!** E.g. asynchronous reading and writing from/to the same buffer by two different processes.

# Sufficient condition for correctness

(C0)  $\text{ample}_s = \emptyset \iff \text{en}_s = \emptyset$

(C1) if  $\text{ample}_s \neq \text{en}_s$  then every  $\alpha \in \text{ample}_s$  is invisible

(C2) ?  $(\text{en}_s \setminus \text{ample}_s) \perp \text{ample}_s$

(C3) ...

**Idea:** Instead of doing sth now, do it in future!

(C2) a transition dependent on some transition from  $\text{ample}_s$   
can not be executed  
before some transition from  $\text{ample}_s$  is executed

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(C2) for every path  $\Pi$  starting in  $s$ :

if  $\alpha \in \text{ample}_s$ ,  $\beta \notin \text{ample}_s$ ,  $\alpha D \beta$

then  $\beta$  can not be executed in  $\Pi$

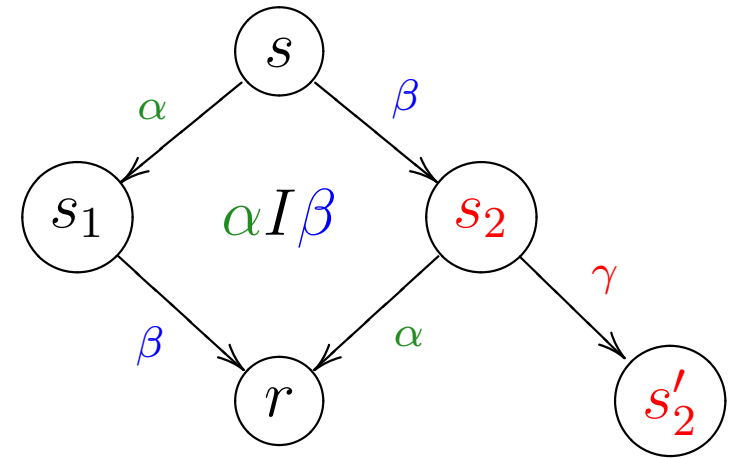
before some transition from  $\text{ample}_s$  is executed

**Lemma:** (C2) implies  $(\text{en}_s \setminus \text{ample}_s) \not\subseteq \text{ample}_s$ .

**Proof:** Let  $\beta \in \text{en}_s \setminus \text{ample}_s$ ,  $\alpha \in \text{ample}_s$ ,  $\alpha D\beta$ .

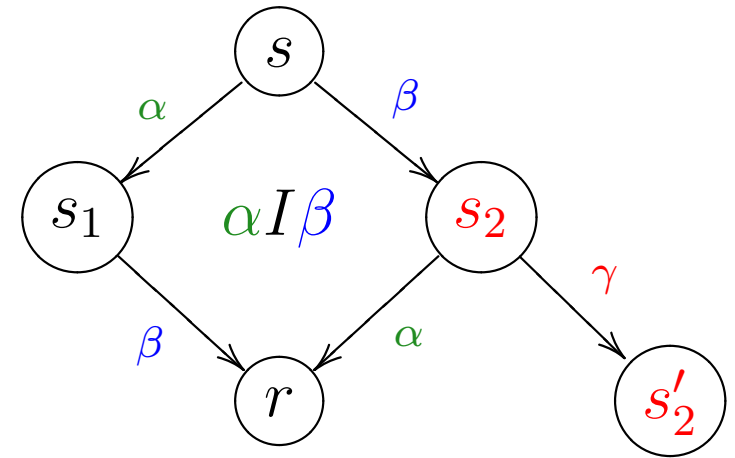
$s \xrightarrow{\beta} \beta(s) \rightarrow \dots$  contradiction with (C2) .

**Problem 2:**  $s_2$ —successors unreachable otherwise.



e.g., let  $\alpha \in \text{ample}_s, \beta \notin \text{ample}_s$

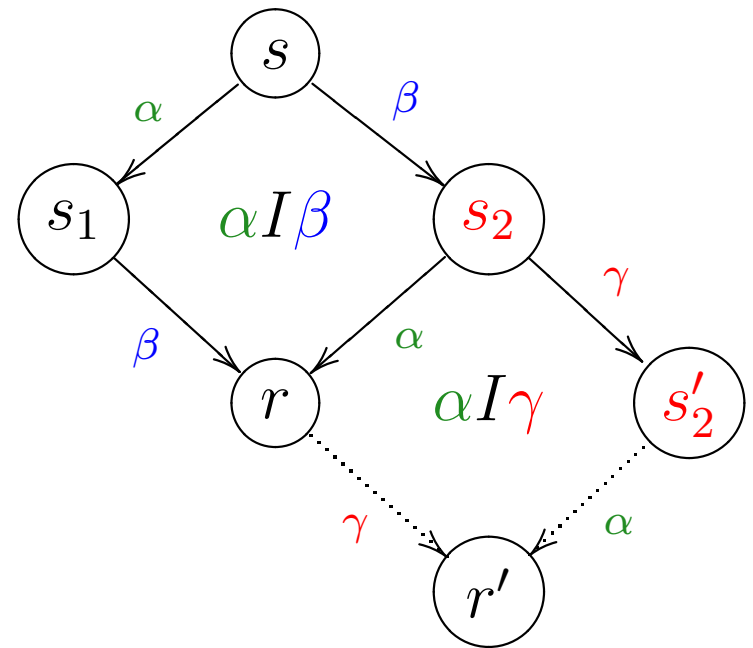
**Problem 2:**  $s_2$ —successors unreachable otherwise.



e.g., let  $\alpha \in \text{ample}_s$ ,  $\beta \notin \text{ample}_s$

by **(C2)** applied to  $\beta\gamma \dots$ , we deduce  $\gamma I \alpha$

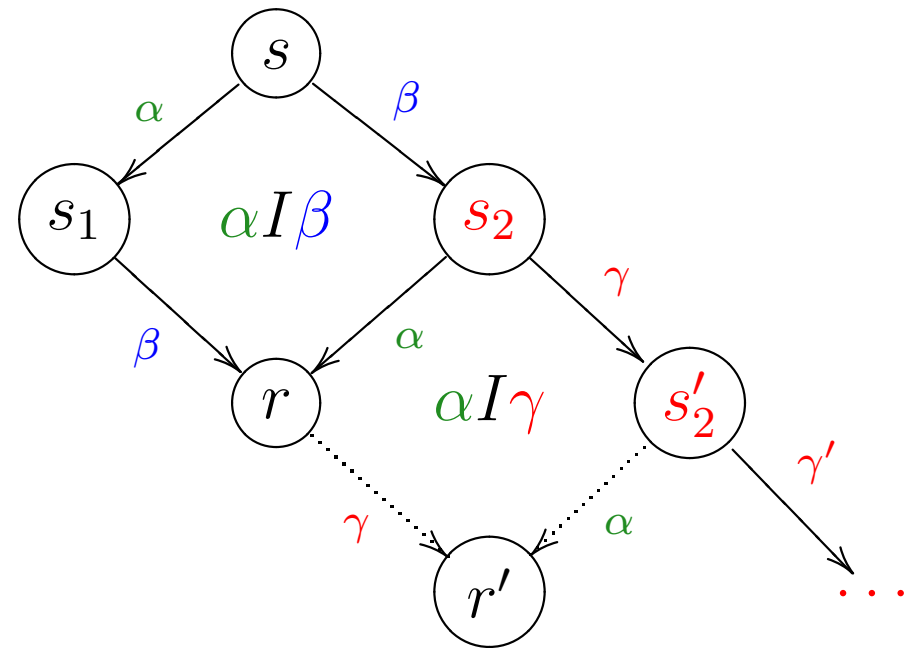
**Problem 2:**  $s_2$ —successors unreachable otherwise.



$\alpha$  invisible, thus  $ss_1rr' \equiv ss_2s'_2$



**Problem 2<sup>∞</sup>:**  $s_2$ —path unreachable otherwise.

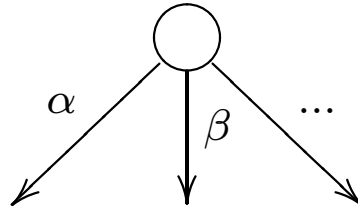


by **(C2)** we deduce  $\gamma I \alpha$ ,  $\gamma' I \alpha$ , ...

$\alpha$  invisible, thus  $s s_1 r r' \dots \equiv s s_2 s'_2 \dots$

**Def. (weak fairness):** if  $\alpha \in \text{en}_s$  almost always then  $\alpha$  eventually executed.

**Corollary:** for every reachable state  $s$ , if  $\alpha \in \text{en}_s$  then eventually some  $\beta$  will be executed such that  $\alpha D \beta$ .

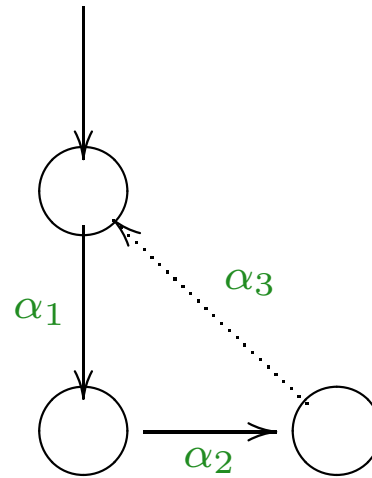
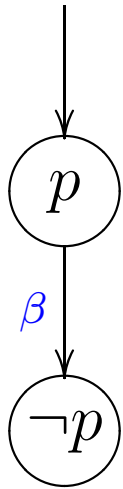


Problem  $2^\infty$  does not appear under weak fairness

Are (C0) – (C2) sufficient?

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No!



(C3) we forbid cycles  $C$  such that  $\exists \beta \forall s \in C \beta \in \text{en}_s \setminus \text{ample}_s$

# Sufficient condition for correctness

**(C0)**  $\text{ample}_s = \emptyset \iff \text{en}_s = \emptyset$

**(C1)** if  $\text{ample}_s \neq \text{en}_s$  then every  $\alpha \in \text{ample}_s$  is invisible

**(C2)** for every path  $\Pi$  starting in  $s$ :

if  $\alpha \in \text{ample}_s$ ,  $\beta \notin \text{ample}_s$ ,  $\alpha D \beta$

then  $\beta$  can not be executed in  $\Pi$

before some transition from  $\text{ample}_s$  is executed

**(C3)** we forbid cycles  $C$  such that  $\exists \beta \forall s \in C \beta \in \text{en}_s \setminus \text{ample}_s$

How to implement this?

# Sufficient condition for correctness

(C1) easy

(C2) hard, implemented in an approximate manner

- an over-approximation of  $D$  is computed
- condition (C2) is monotonic
- static analysis only

(C3) replaced by an easier but stronger:

(C3') if  $\text{ample}_s \neq \text{en}_s$  then  $\forall \alpha \in \text{ample}_s \alpha(s) \notin \text{stack}$

## Implementation decision:

$\text{ample}_s =$  all transitions of some process  $i$  enabled in  $s$



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$\text{ample}_s =$  all transitions of some process  $i$  enabled in  $s$

whenever

- they are **independent** from all operations of all other processes
- no operation of any other process may **enable** any other operation of process  $i$

# $\beta$ enabling $\alpha$ (over-approximation)

- if  $\beta$  modifies pc so that  $\alpha$  may be executed
- if **Promela enabling condition** for  $\alpha$  depends on global variables, then any  $\beta$  that modifies these variables
- if  $\alpha$  is reading from/writing to a buffer then any  $\beta$  that reads from/writes to this buffer

## $\alpha D \beta$ (over-approximation)

- $\alpha$  i  $\beta$  refer to the same global variable  
and at least one of them modifies the variable (over-appr.)
- $\alpha$  i  $\beta$  belong to the same process; synchronous communication is understood as belonging to both processes
- $\alpha$  i  $\beta$  write to/read from the same buffer

However reading from and writing to the same buffer is independent!

# What remains independent?

## Example:

Operations independent from all operations of other processes:

- operating on local variables
- reading from a buffer with **xr** flag set
- writing to a buffer with **xs** flag set
- test `nempty(q)` if **xr** flag is set for `q`
- test `nfull(q)` if **xs** flag is set for `q`

# P.-o. reductions and on the fly verification

- in both DFS's the set  $\text{ample}_s$  should be the same
- condition **(C3')** is applied to  $M \times \mathcal{A}_{\neg\phi}$  instead of  $M$ .

Is it correct?